

REMARKS

Claims 1-10, 15-16, 18-19, 21 and 22 are pending in the application. Claims 2-7 and 10 are found to contain allowable subject matter. With regard to claim 2 it is listed as allowable and also listed as rejected by Brokish. Applicant submits claim 2 is allowable for at least the reasons listed below.

Claims 1, 3, 4, 6-8, 11, 14-23, are amended herein. Also please note that claims 11, 14, 17, 20, and 23, that are withdrawn from consideration, are amended together with the pending claims.

Applicant has amended the claims in order to clarify the claimed invention. The current amendment is basically in accordance with the examiner's suggestion with several additional clarifications. The term "previous data" is changed to "first input data", and the term "present data" is changed to "second input data." The second input data is inputted after the first input data.

Claim Rejections

Claims 1-10, 15-16, 18-19 and 21-22 are rejected under 35 U.S.C. §112, second paragraph, as being indefinite.

The claims have been clarified as mentioned above. Because applicant has amended the claims to more clearly describe what was inherently present, it is respectfully requested the rejection be withdrawn.

Claims 1-2, 8-9, 15-16, 18-19, and 21-22 stand rejected as being anticipated by Brokish under 35 U.S.C. §102(e).

It is respectfully submitted that Brokish fails to disclose each and every feature recited in claims 1-2, 8-9, 15-16, 18-19, and 21-22.

For example applicant's claim 1 includes an adding unit for adding results of the first and second operational units and outputting the resultant as a filter response.

The Office Action points to Brokish unit 230 for the second operating unit however the adding unit 210 is not connected to the output of the second operational unit 230. (see Figs. 1A and 1B and col. 5, lines 2-6 of Brokish).

Col. 5, lines 2-6 of Brokish teaches: "Accumulator Y (ACy) 244 holds the output of MAC2230 which includes the running total of the FIR filter output. Accumulator X (ACx) 246 holds the output of MAC1220 until it is used in ALU 210. Several buses are used with this implementation of the present invention. A coefficient read bus 258 contains the current clock cycle's coefficient and provides it to MPY 236 of MAC2230 and arithmetic unit 212 of ALU 210."

For at least the foregoing reasons Brokish fails to teach each and every feature of the claimed invention because each of the independent claims includes at least the above distinguishing features.

Applicant's claims 2 and 9 also include the data separation unit which is not disclosed in Brokish. The Office Action point to 261 and 270 in Brokish to teach this feature.

However Brokish describes each of these elements hold data different from each other. In contrast applicant claims a data separation unit for separating data inputted to the filter into said input data and said additional data. Brokish does not disclose any units to separate input data held in 261 and 270.

Brokish describes in col. 5 last paragraph: At the beginning of each loop, the accumulators are cleared and a delayed error term is stored in temporary register 270. This term is preferably the error calculated based on the FIR filter output from the previous data sample,

i.e. during the previous frame. The error is thus delayed one frame. The term in temporary register 270 could be the calculated error e itself (i.e. the difference between the desired output and the FIR filter output during the previous frame).

It is respectfully submitted claims 2 and 9 are distinguished from the cited reference.

In view of the remarks set forth above, this application is in condition for allowance which action is respectfully requested. However, if for any reason the Examiner should consider this application not to be in condition for allowance, the Examiner is respectfully requested to telephone the undersigned attorney at the number listed below prior to issuing a further Action.

Any fee due with this paper may be charged to Deposit Account No. 50-1290.

Respectfully submitted,



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